



APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

Title of Invention	High Voltage Circuits Implemented Using Low Voltage Transistors		
Application Type : regular, utility Attorney Docket Number : TI-37611			
Correspondence address: Customer Number: 23494 			
Inventor Information: <u>Inventor 1:</u> Applicant Authority Type: Inventor Citizenship: IN Name prefix: Mr. Given Name: Sanish Middle Name: Koshi Family Name: JACOB Residence: City of Residence: Trivandrum Country of Residence: IN Address-1 of Mailing Address: 8-H, Saraswathi Nilayam Address-2 of Mailing Address: Jawahar Nagar City of Mailing Address: Trivandrum State of Mailing Address: Postal Code of Mailing Address: 695 041 Country of Mailing Address: IN Phone: (972) 917-4371 Fax: (972) 917-4418 E-mail:			
Attorney Information: practitioner(s) at Customer Number: 23494  as my attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.			

Publication Information:
Suggested Figure for Publication -
Suggested Classification -
Suggested Technology Center -
Total Number of Drawing Sheets - 20

Assignee 1:

Organization Name:	Texas Instruments Incorporated
Address-1 of Mailing Address:	P. O. Box 655474
Address-2 of Mailing Address:	MS 3999
City of Mailing Address:	Dallas
State of Mailing Address:	TX
Postal Code of Mailing Address:	75265
Country of Mailing Address:	US
Phone:	(972) 917-4371
Fax:	(972) 917-4418
E-mail:	